PTO/SB/08a 07-05)
Approved for use through 07/31/2006. OMB 0651-0031
U. S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

Substitute for form 1449A/PTO				Complete if Known		
INE	ORMATION	DISC	CLOSURE	Application Number	10/757,851	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Filing Date	January 16, 2004	
STATEMENT BY ATTECANT			LICHNI	First Named Inventor	Craig C. HANSEN, et al.	
(use as many sheets as necessary)				Group Art Unit	2183	
				Examiner Name	COLEMAN, ERIC	
Sheet	1	of	1	Attorney Docket Number	43876-162	

		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
Examiner nitials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T ²
		Control No. 90/007,583 (Reexam of US 5,742,840) Supplemental Response filed October 31, 2008	
	<u>. </u>	MJB08274EP Extended European Search Report dated November 26, 2008 in Application No. / Patent No. 07112545.4-1243 / 1879103	
		MJB08275EP Extended European Search Report dated November 11, 2008 in Application No. / Patent No. 07112548.8-1243 / 1879398	
		Greenley et al., "UltraSPARC(TM): The Next Generation Superscaler 64-bit SPARC," IEEE, 442-51 (1995) (XP000545452)	
		Gwennap, "UltraSPARC Adds Multimedia Instructions," <i>Microprocessor Report</i> , vol.8, no.6, 1-3 (Dec. 5, 1994), MicroDesign Resources © (1994) (XP000561690)	
	-	Kohn et al., "The Visual Instruction Set (VIS) in UltraSPARCTM," IEEE, 462-469 (1995) (XP000545454)	
		Shipnes, "Graphics Processing with the 88110 RISC Microprocessor," <i>IEEE COMPCON SPRING</i> '92, 169-74 (Feb. 24-28, 1992) (XP000340730)	
,		Zhou et al., "MPEG Video Decoding with the UltraSPARC Visual Instruction Set," IEEE, 470-75 (1995) (XP002472254)	
		Zucker, Daniel F. et al., Reuse of High Precision Arithmetic Hardware to Perform Multiple Concurrent Low Precision Calculation, IEEE, April 1994	
 		Chart: MicroUnity Media Processor Patent Family	
			-
			1
			-

Examiner	Date	
Signature	Considered	

^{*}EXAMINER: Initial reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P. O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORM TO THIS ADDRESS. Send To Commissioner For Patents, P. O. Box 1450, Alexandria, VA 22313-1450. If you need assistance in completing the form call 1.800-PTO-9109 and select conting 2. completing the form, call 1-800-PTO-9199 and select option 2.